



PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10004976	12/03/2001	700-	21	2421	Garbowski

APPLICANTS: Rategh Hamid; Soltan Mehdi;

CONTINUING DATA VERIFIED:

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FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	M-12366 US
Verified and Acknowledged Examiners' initials	
TITLE : integrated circuits with scalable design	

U.S. DEPT. OF COMMERCE PAT. & TRADEMARK OFFICE

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistant Examiner		Total Claims	Print Claim for O.G.
Primary Examiner		DRAWING	
Application Examiner		Sheets Drwg.	Figs. Drwg.
PREPARED FOR ISSUE		Print Fig.	
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